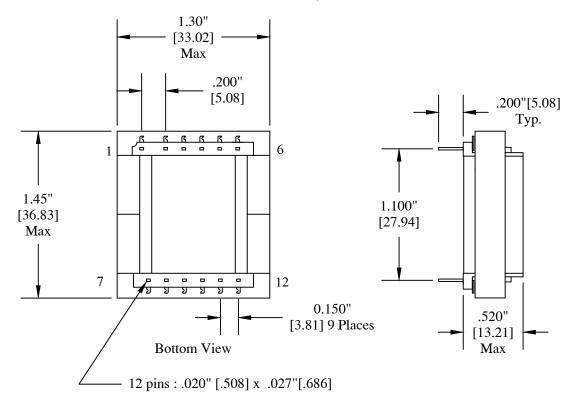
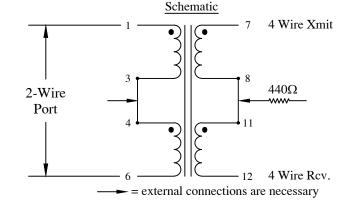


- OPERATING LEVEL -45 TO +7dBm.
- DESIGNED FOR 0 DC CIRCUITS.
- CAN BE CONNECTED AS A DUAL-HYBRID FOR HIGHER PERFORMANCE.
- UL RECOGNIZED COMPONENT UL 1863, FILE E138250.



All dimensions are reference unless otherwise specified.



REVISIONS			
DATE	REV	DESCRIPTION	APPV'D
11/27/19	-1	Corrected pin numbers in schematic; Added where the DC resistance values are measured;	TJK

Electrical Parameters:

2-Wire Impedance: 900Ω

4-Wire Impedances: 600Ω each typical

Turns Ratio: 1:1.257

D.C. Resistance of 2-Wire Wdg (1 - 6): 120.0Ω typical D.C. Resistance of 4-Wire Wdg (7 - 12): 137Ω total typical Maximum unbalance D.C. Current (2-Wire side): 0mA Dielectric Strength 1500Vrms: PRI - SEC - CORE Frequency Response 300Hz - 3500Hz: +/- 0.5dB

Hybrid Loss: 4.5dB Typical @300Hz Primary Return Loss 1200Ω Load:

- ≥20dB@300Hz
- \geq 25dB@1KHz
- \geq 28dB@4KHz

THD @0dBm 300Hz: -65dB typ. 1200Ω Load 7-12 (tie 8 - 11)

Trans-Hybrid Balance (440Ω Balance resistor)

- >23dB@300Hz
- >32dB@1KHz

Longitudinal Balance: 60dB min per IEEE 455-1985

